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NOTICE OF ALLOWANCE AND FEE(S) DUE

181 7590 03/11/2009 MILES & STOCKBRIDGE PC 1751 PINNACLE DRIVE

MCLEAN VA 22102-3833

SUITE 500

EXAMINER

COLE, BRANDON S

ART UNIT PAPER NUMBER

2816

MCLEAN, VA 22102-3855 DATE MAILED: 03/11/2009

 APPLICATION NO.
 FILING DATE
 FIRST NAMED INVENTOR
 ATTORNIEY DOCKET NO.
 CONFRMATION NO.

 10/584,395
 06/23/2006
 Keiko Fukuda
 XA-10578
 9000

TITLE OF INVENTION: VOLTAGE GENERATION CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

 APPLN.TYPE
 SMALL ENTITY
 ISSUE FEE DUE
 PUBLICATION FEE DUE
 PREV. PAID ISSUE FEE
 TOTAL FEE(S) DUE
 DATE DUE

 nonprovisional
 NO
 \$1510
 \$300
 \$0
 \$1810
 06/11/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 1SI. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FFE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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appropriate. All further indicated unless correcte maintenance fee notifical	correspondence includir ed below or directed oth	or transmitting the 188 ig the Patent, advance of herwise in Block 1, by	orders and notification of a (a) specifying a new corre	maintenance fees w spondence address;	ill be and/o	mailed to the current r (b) indicating a sepa	correspondence address as arate "FEE ADDRESS" for
CURRENT CORRESPONDE	Not Fee pap	Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying pagers. Each additional pager, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.					
1751 PINNACL SUITE 500		/2009		Cor	tificate	of Mailing on Trans	
MCLEAN, VA	22102-3833						(Depositor's name)
			<u> </u>				(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	:	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.
10/584,395 TITLE OF INVENTION	06/23/2006 : VOLTAGE GENERA	TION CIRCUIT AND S.	Keiko Fukuda EMICONDUCTOR INTEC	GRATED CIRCUIT	DEVI	XA-10578 CE	9000
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0		\$1810	06/11/2009
EXAM	INER	ART UNIT	CLASS-SUBCLASS	1			
COLE, BR.	ANDON S	2816	327-541000	•			
"Fee Address" ind PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME A PLEASE NOTE: Uni recordation as set forti	ondence address (or Cha 3/122) attached. ication (or "Fee Address 12 or more recent) attach ND RESIDENCE DATZ less an assignee is ident h in 37 CFR 3.II. Comp	inge of Correspondence Indication form and. Use of a Customer A TO BE PRINTED ON		o 3 registered paten vely, le firm (having as a agent) and the name meys or agents. If a printed.	membes of uno nan	er a 2p to p to see is 3	ocument has been filed for
(A) NAME OF ASSIC		categories (will not be p	(B) RESIDENCE: (CITY				oup entity Government
4a. The following fee(s): Issue Fee Publication Fee (N Advance Order - 4	o small entity discount p		Hb. Payment of Fee(s): (Plea A check is enclosed. Payment by credit can The Director is hereboverpayment, to Depo	rd. Form PTO-2038	is atta	iched.	shown above) ficiency, or credit any n extra copy of this form).
	s SMALL ENTITY state	as. See 37 CFR 1.27.	b. Applicant is no lon				
NOTE: The Issue Fee and interest as shown by the	d Publication Fee (if req records of the United Sta	uired) will not be accept ites Patent and Trademar	ed from anyone other than k Office.	the applicant; a regi	stered	attorney or agent; or th	ne assignee or other party in
Authorized Signature				Date			
Typed or printed name				Registration N			
This collection of inform an application. Confident submitting the complete this form and/or suggesti Box 1450, Alexandria, V Alexandria, Virginia 223	ation is required by 37 C tiality is governed by 35 d application form to the ons for reducing this but firginia 22313-1450. DC 13-1450.	CFR 1.311. The informat U.S.C. 122 and 37 CFR USPTO. Time will var rden, should be sent to to O NOT SEND FEES OR	ion is required to obtain or 1.14. This collection is es y depending upon the indi- he Chief Information Offic COMPLETED FORMS T	retain a benefit by the timated to take 12 revidual case. Any co er, U.S. Patent and O THIS ADDRESS	he pub ninuter mmen Trader	lic which is to file (and is to complete, includir is on the amount of ti- nark Office, U.S. Dep D TO: Commissioner	d by the USPTO to process) g gathering, preparing, and me you require to complete artment of Commerce, P.O. for Patents, P.O. Box 1450,

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10/584,395	06/23/2006	Keiko Fukuda	XA-10578	9000		
181 75	590 03/11/2009		EXAMINER			
MILES & STOC	KBRIDGE PC	COLE, BRANDON S				
1751 PINNACLE	DRIVE	ART UNIT	PAPER NUMBER			
SUITE 500 MCLEAN, VA 22	102-3833	2816				

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 14 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 14 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address-All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included
herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS
NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative
of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

- 1. This communication is responsive to amendments filed on January 8th 2009.
- The allowed claim(s) is/are 7, 10 15, 17, and 18.
- 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a)

 All b)

 Some* c)

 None of the:
 - 1. A Certified copies of the priority documents have been received.
 - 2. Certified copies of the priority documents have been received in Application No. _____
 - Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

- 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
- 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

 DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- 1. Notice of References Cited (PTO-892)
- Notice of Draftperson's Patent Drawing Review (PTO-948)
- Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 10/06/2008
- Examiner's Comment Regarding Requirement for Deposit of Biological Material
- 5. Notice of Informal Patent Application
- Interview Summary (PTO-413), Paper No./Mail Date .
- 7.

 Examiner's Amendment/Comment
- 8. X Examiner's Statement of Reasons for Allowance

Application/Control Number: 10/584,395 Page 2

Art Unit: 2816

NOTICE OF ALLOWANCE

Allowance in response to amendments filed on 1/08/2009.

Claims 7, 10 -15, 17, and 18 are allowed.

Reasons for Allowance

3. The following is an examiner's statement of reasons for allowance:

Claims 7, 15, and 17 are allowable over the prior art of record because the art of record does not disclose or suggest obvious semiconductor integrated circuit device which includes a CMOS circuit having a second conductive-type well region and a first conductive-type well region formed on a first conductive-type semiconductor substrate, a first conductive-type MOSFET formed on the second conductive-type well region, and a second conductive-type MOSFET formed on the first conductive-type well region, wherein the second conductive-type well region electrically separates the first conductive-type well region on which the second conductive-type MOSFET is formed from the first conductive-type semiconductor substrate, and wherein each of the first transistor and the second transistor is formed of a bipolar transistor having a vertical structure with a second conductive-type diffusion layer, which is formed in a step for forming source and drain diffusion layers of the second conductive-type MOSFET, as an emitter portion, with a portion of a first conductive-type well arrangement, on which the second conductive-type diffusion layer constituting the emitter portion is formed, as

Application/Control Number: 10/584,395

Art Unit: 2816

a base portion, and with a portion of a second conductive-type well arrangement, which electrically separates said portion of the first conductive-type well arrangement from the first conductive-type semiconductor substrate, as a collector portion, in combination with the rest of the claimed limitations.

Claims 10 – 14, and 18 are allowable over the prior art of record because the art of record does not disclose or suggest obvious a semiconductor integrated circuit device includes a CMOS circuit having a second conductive-type well region and a first conductive-type well region formed on a first conductive-type semiconductor substrate. a first conductive-type MOSFET formed on the second conductive-type well region, and a second conductive-type MOSFET formed on the first conductive-type well region. wherein each of the first transistor and the second transistor of the reference voltage generating circuit is formed of a bipolar transistor having a lateral structure with collector, emitter, and base portions being constituted by diffusion layers formed on respective portions of a first conductive-type well arrangement, the diffusion layers of the collector and emitter portions being formed in a step for forming source and drain diffusion layers of the second conductive-type MOSFET, wherein the first conductivetype is a p-type and the second conducive-type is an n-type, wherein the power source voltage supplied from the second external terminal is positive, wherein the second transistor is constituted by one unit transistor, and wherein the first transistor is constituted by connecting a plurality of unit transistors corresponding to the second transistor in parallel, in combination with the rest of the claimed limitations.

Art Unit: 2816

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to BRANDON S. COLE whose telephone number is (571)270-5075. The examiner can normally be reached on Mon - Fri 7:30-5:00 EST (Alternate Friday's Off).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lincoln Donovan can be reached on (571) 272-1988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/584,395 Page 5

Art Unit: 2816

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Long Nguyen/ Primary Examiner, Art Unit 2816

/Brandon S Cole/ Examiner, Art Unit 2816